



Express Mail No. EV170137842US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Scott Parkhill et al. ✓
Application No. : 09/882,708 ✓
Filed : June 15, 2001 ✓
For : LEADFRAME-BASED MODULE DC BUS DESIGN TO REDUCE
MODULE INDUCTANCE

TECHNOLOGY CENTER 2800

SEP - 9 2002

RECEIVED

Art Unit : 2838
Docket No. : 130209.430
Date : September 5, 2002

Commissioner for Patents
Washington, DC 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references, as required under 37 C.F.R. § 1.98, are enclosed).

Applicants note that reference AG (EP 0 427 143 A2) is written in German. Accordingly, the Examiner's attention is respectfully directed to reference AM which sets forth an English language abstract.

As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

We hereby certify that each of the references set forth on the attached form PTO-1449 was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

Scott Parkhill et al.

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Enclosures:

Postcard

Form PTO-1449

Cited References (5)

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| FORM PTO-1449 (REV. 7-80) | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | ATTY. DOCKET NO. 130209.430 | | APPLICATION NO. 09/882,708 | |
| SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | | | | APPLICANT Scott Parkhill et al. | | | |
| | | | | FILING DATE June 15, 2001 | | GROUP ART UNIT 2838 | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| *EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
| | AA | 5,422,440 | 06/06/95 | Palma | 174 | 133 | |
| | AB | | | | | | |
| | AC | | | | | | |
| | AD | | | | | | |
| | AE | | | | | | |
| | AF | | | | | | |
| FOREIGN PATENT DOCUMENTS | | | | | | | |
| | | DOCUMENT NUMBER | DATE | COUNTRY | TRANSLATION | | |
| | | | | | YES | NO | |
| | AG | EP 0 427 143 A2 | 05/15/91 | EPO | | X | |
| | AH | EP 0 578 108 A1 | 01/12/94 | EPO | | | |
| | AI | | | | | | |
| | AJ | | | | | | |
| | AK | | | | | | |
| | AL | | | | | | |
| OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | |
| | AM | English Abstract of EP 0 427 143, esp@cenet, ep.espacenet.com , May 15, 1991. | | | | | |
| | AN | Mohan et al., <i>Power Electronics: Converters, Applications and Design</i> , John Wiley & Sons Inc., United States, 1989, Chapter 26-8, "Circuit Layout", p. 654. | | | | | |
| | AO | | | | | | |
| | AP | | | | | | |
| | AQ | | | | | | |
| | AR | | | | | | |
| EXAMINER | | | | DATE CONSIDERED | | | |
| * EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s). | | | | | | | |